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	Application No.	Applicant(s)
Notice of Allowability	10/620,124	DOERR, CHRISTOPHER RICHARD
	Examiner	Art Unit
	PHAN T.H. PALMER	2874
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this apport or other appropriate communication GHTS. This application is subject to	plication. If not included will be mailed in due course. THIS
1. This communication is responsive to <u>07/15/2003</u> .		•
2. The allowed claim(s) is/are <u>1-20</u> .		
3. $igotimes$ The drawings filed on <u>15 July 2003</u> are accepted by the Ex	aminer.	
4. Acknowledgment is made of a claim for foreign priority unexative and a claim for foreign priority documents have a claim for foreign priority documents have a claim for foreign priority and a claim for foreign priority unexative and a claim for foreign priority and a claim for foreign priority and a claim foreign priority documents have a claim forei	been received in Application Nocuments have been received in this of this communication to file a reply ENT of this application.  Itted. Note the attached EXAMINER as reason(s) why the oath or declarate the submitted.  It be submitted.  It is application on the Comment or in the Comment of the Comment	national stage application from the complying with the requirements  'S AMENDMENT or NOTICE OF stion is deficient.  948) attached  Office action of the back) of d).  nust be submitted. Note the
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/06 Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. Interview Summary Paper No./Mail Dat 8), 7. Examiner's Amendo	e

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## **REASONS FOR ALLOWANCE**

1. The following is an examiner's statement of reasons for allowance: the claims are allowable over prior art of record, because none of the references in alone or in combination discloses:

+++ An optical chip, and a method comprising: waveguide integrated on the optical chip, the waveguides having varied widths dependent upon how many waveguides each of the waveguide cross with respect to each other, as discloses in claims 1 and 17.

## +++ An optical chip, comprising:

- a first waveguide having width X;
- a second waveguide having a width Y, the second waveguide crossing the first waveguide; and
- a plurality of other waveguides, wherein a greater quantity of the plurality of other waveguides cross the first waveguide than the second waveguide, and wherein the width X for the first waveguide is wider than the width Y for the second waveguide, as discloses in claim 6.

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+++ An optical chip, and a method comprising:

- a first waveguide;

- a second waveguide that cmsses the first waveguide;

- a plurality of other waveguides, wherein a greater quantity of the

plurality tlf other waveguide cross the first waveguide than the second

waveguide; and

- a void inserted in vicinity of the crossing between the first and second

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waveguide, the void configured to reduce optical losses in the first waveguide

and simultaneously increase optical losses in the second waveguide, such that

the overall optical losses between the first and second waveguides are

substantially equalized with respect to each other, as discloses in

claims 13 and 19.

The underline limitations are critically, since the optical chip is improved:

to equalization and reduction of optical losses in waveguides associated with

waveguide crossings.

Any comments considered necessary by applicant must be submitted no

later than the payment of the issue fee and, to avoid processing delays, should

preferably accompany the issue fee. Such submissions should be clearly labeled

"Comments on Statement of Reasons for Allowance."

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## **CONTACT INFORMATION**

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2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to PHAN T.H. PALMER whose telephone number is (571) 272-2354. The examiner can normally be reached on 4/4 OFF MONDAY.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, RODNEY B. BOVERNICK can be reached on (571) 272-2344. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PTHP 01/26/2005

PHAN T. H. PALMER PRIMARY EXAMINER